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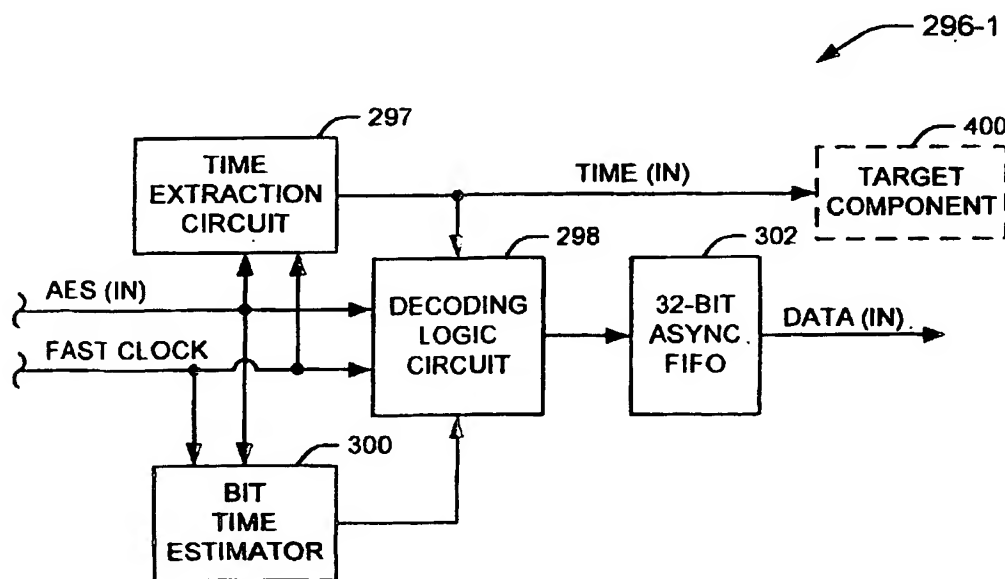
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(54) Title: **CLOCK EXTRACTION CIRCUIT FOR USE IN A LINEARLY EXPANDABLE BROADCAST ROUTER**



(57) Abstract: A method for extracting selected time information from a stream of serialized AES digital audio data. A first transition (354) indicative of a first preamble of said stream of serialized AES digital audio data is detected and, upon detection of the transition, a time count (355) initiated. A second transition (360) indicative of a subsequent preamble of said serialized AES digital audio data is subsequently detected and the time count halted. The time separating the first and second transitions is then determined. The separation time, which preferably is determined in the form of a fast clock pulse count (362), is then transferred to a decoding logic circuit (298) for use in decoding the stream of serialized AES digital audio data.



— *before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments*

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INTERNATIONAL SEARCH REPORT

International application No.

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A. CLASSIFICATION OF SUBJECT MATTER

IPC(7) : H04L 27/06; H03D 1/00

US CL : 375/340

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 375/340

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 5,404,362 (MEITNER) 04 April 1995 (4.4.1995) Column 1, Lines 39-51 Fig 5 Column 3, Lines 29-39	1-3; 6-7; 10-11
A	US 5,889,820 A (ADAMS) 30 March 1999 (30.4.1999) Column 3, Line 60-61 Abstract	1-11

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